



REPLACEMENT SHEET

	0	4	6	2	8	12	13	10	
CLK ₀	0	0	0	0	0	0	0	0	n*16 bit inputs
CLK ₁	0	0	1	1	0	0	1	1	
CLK ₂	0	1	1	0	0	1	1	0	
CLK ₃	0	0	0	0	1	1	1	1	
	n=2								

	0	4	6	3	1	8	12	13	11	9	
CLK ₀	0	0	0	1	1	0	0	0	1	1	n*20 bit inputs
CLK ₁	0	0	1	1	0	0	0	1	1	0	
CLK ₂	0	1	1	0	0	0	1	1	0	0	
CLK ₃	0	0	0	0	0	1	1	1	1	1	
	n=2										

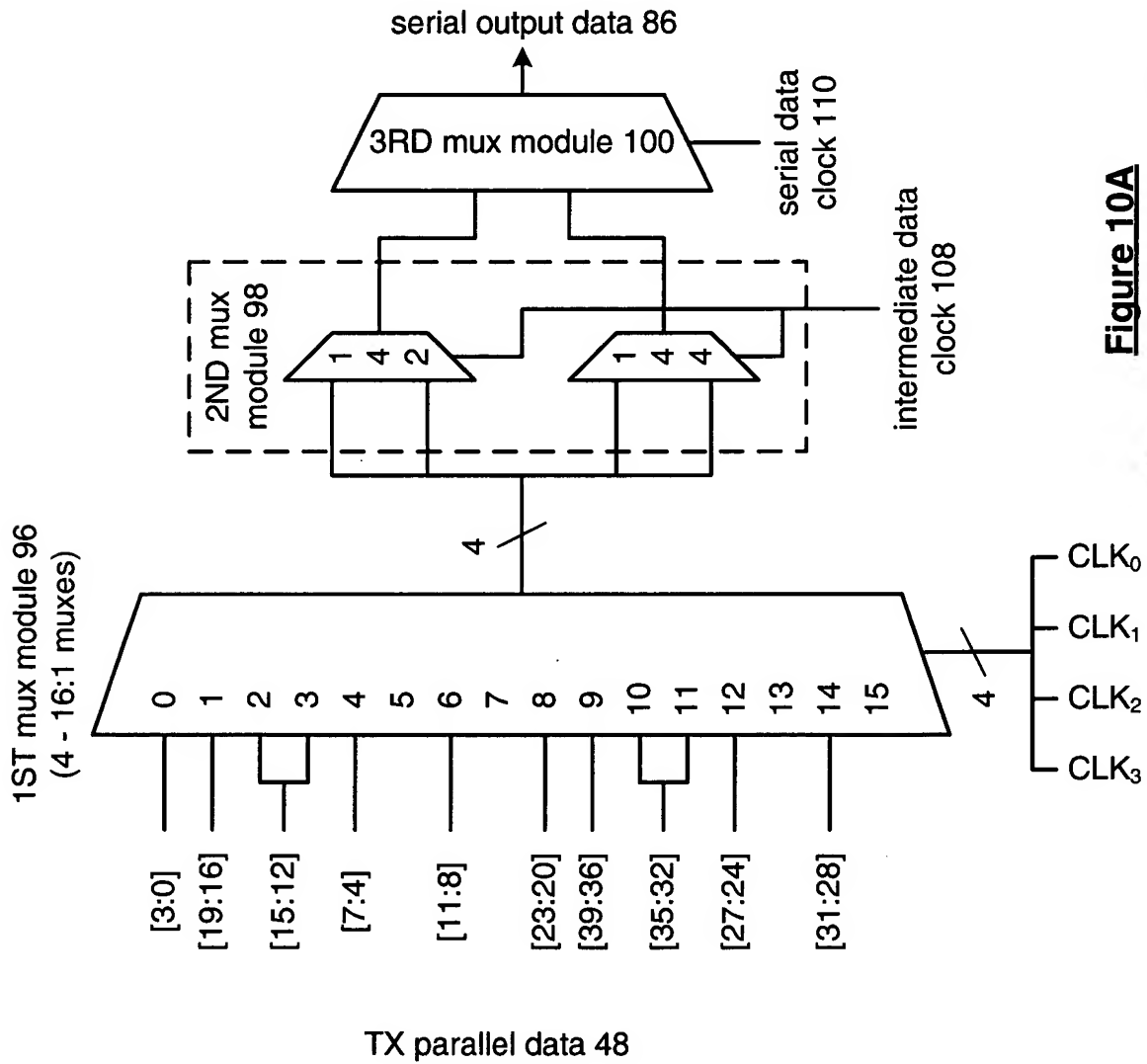


Figure 10A
parallel to serial module 84

REPLACEMENT SHEET

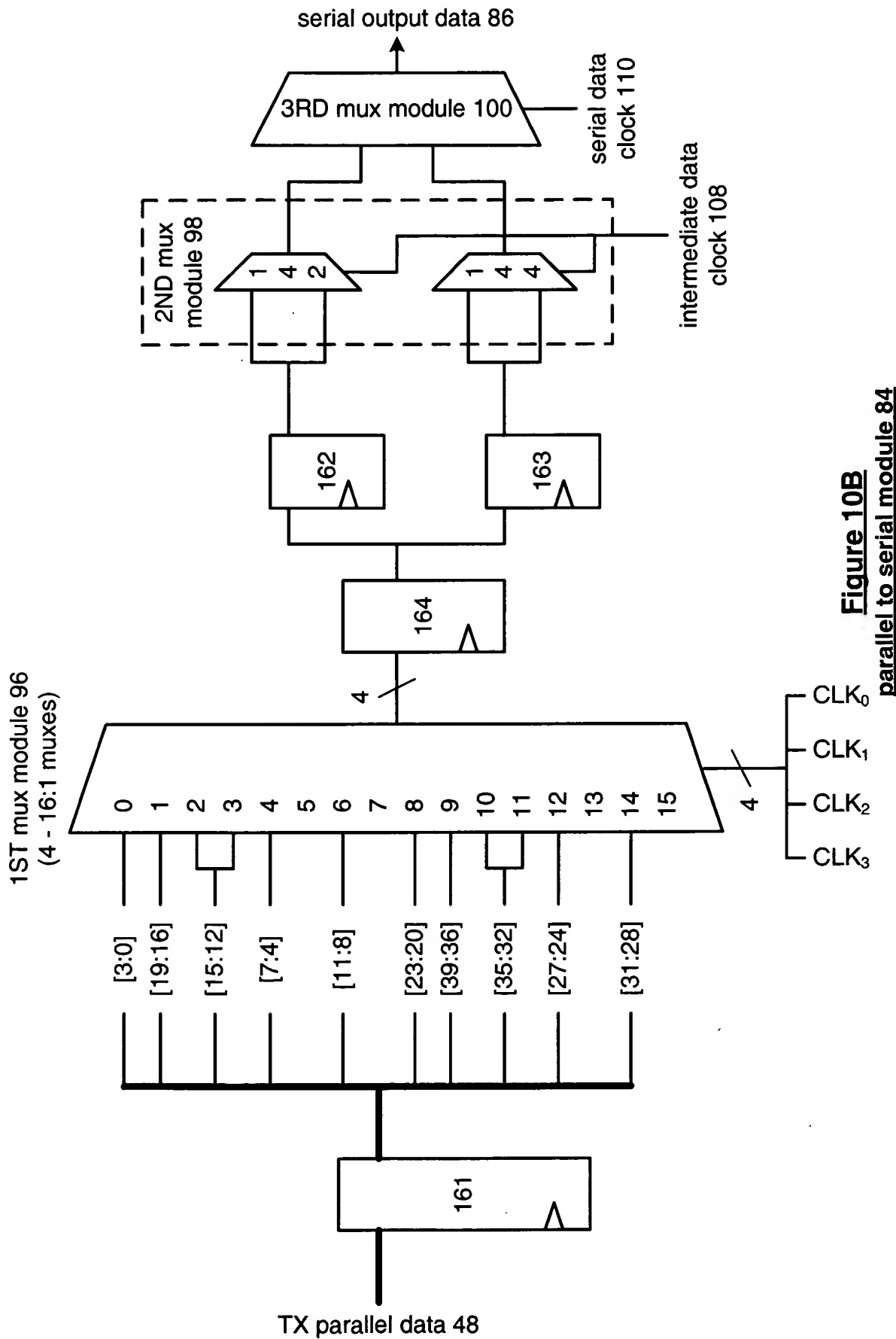


Figure 10B
parallel to serial module 84